Research Topics in Si-based Epitaxy for Industrial Applications
By
Dr. LIU Jin Ping

About the speaker:

Dr. Jinping Liu is a senior principal engineer with Technology Development, Chartered Semiconductor, working on advanced process development. He has been doing research on epitaxy for more than ten years. Prior to joining Chartered, he did postdoctoral research with Lawrence Berkeley National Lab, US, and Institute of Semiconductor Physics, Germany. He has more than 60 publications in refereed journals and conferences and several patents.

Abstract:

Si-based epitaxy has found more and more applications in advanced Si technologies. While some epitaxial processes have been mature enough for high volume manufacturing, there are a lot of unknowns in other new epitaxial processes as well as their integration into Si technologies.

This talk will give a brief summary of our work in this area. We will present our recent results on growth kinetics, integration, as well as engineered substrates.

Date: Friday, 13 Oct 2006
Time: 10.30am to 11.30am
Venue: PAP Meeting Room (SBS B3n-19)